

GORDON
MOORE

NUMBER OF
TRANSISTORS
ON A CHIP
WILL **DOUBLE**
ABOUT EVERY
TWO YEARS

MOORE'S LAW

MOORE'S LAW

COMPUTER SPEED

ROUGHLY

PROPORTIONAL TO

NUMBER OF

TRANSISTORS PER UNIT AREA

COMPUTER SPEED

WILL DOUBLE

ABOUT EVERY TWO YEARS

Million Instructions Per Second

PROCESSING POWER (MIPS)

INCREASED

BECAUSE

TRANSISTOR

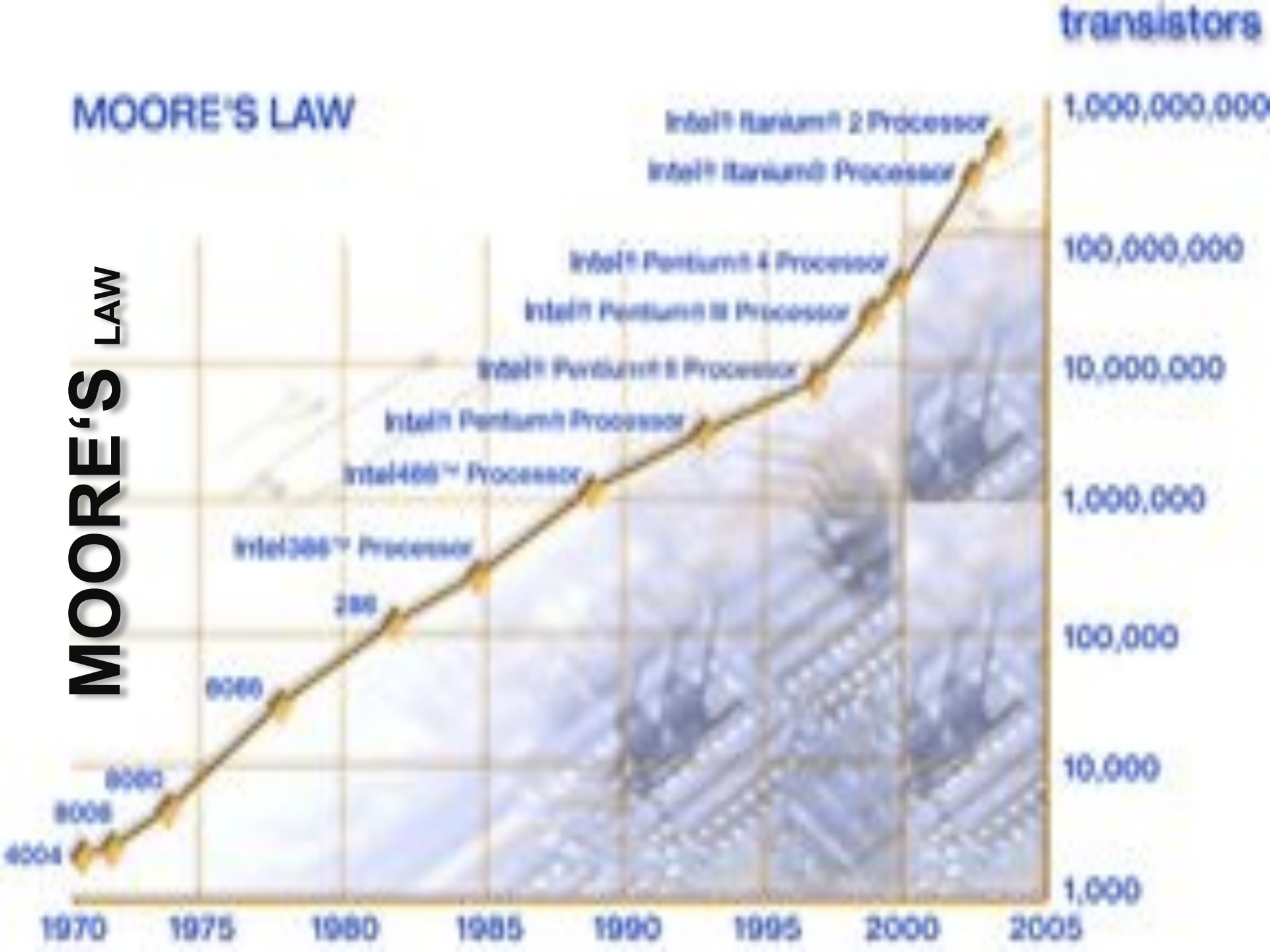
DENSITY

INCREASED

MOORE'S LAW

MOORE'S LAW

MOORE'S LAW



LIMITATIONS

MOORE'S LAW

- * PHYSICAL LIMITS
- * POWER CONSUMPTION
- * HEAT DISSIPATION
- * Some Others

HARDWARE SPEED IS
LIMITED

LIMITATIONS

MOORE'S LAW

- * PHYSICAL LIMITS
- * POWER CONSUMPTION
- * HEAT DISSIPATION
- * Some Others

HARDWARE SPEED IS
LIMITED

IDEA

MOORE'S LAW

SINGLE PROCESSING UNIT !

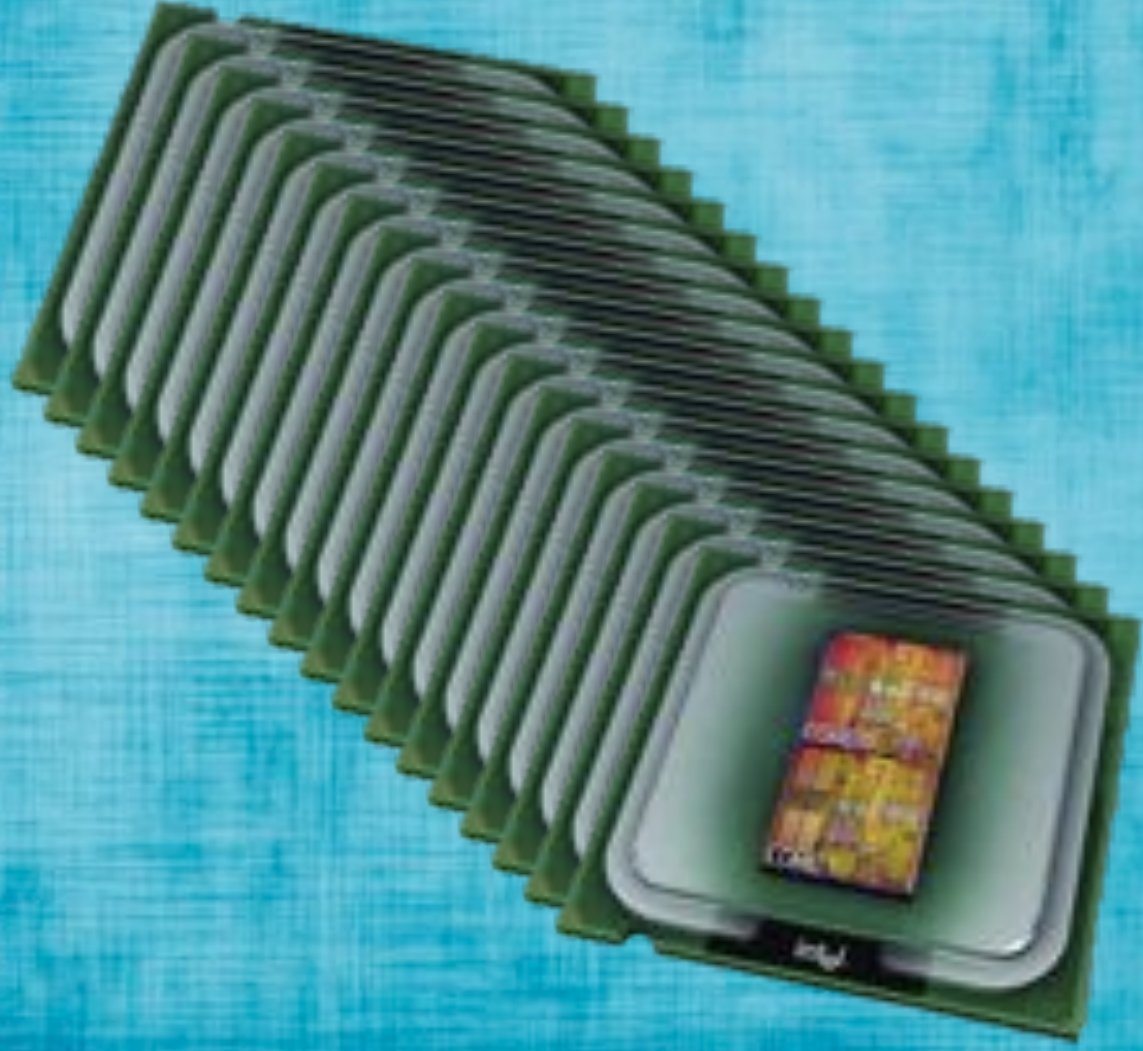
CAN WE USE

MULTIPLE

PROCESSING UNITS

MULTIPLE IS THE SPARK FOR HPC

MULTIPLE PROCESSING UNITS



MULTIPLE PROCESSING UNITS



MacBook MACHINE

Intel Core 2 Duo

2 CORES

4 Giga Byte Ram

Cost \$1000

CAN SOLVE SOME
PROBLEM IN

8 HOURS

SING UNITS



MUL-

CRAY C90 MACHINE

8 VECTOR PROCESSORS

32 GIGA BYTE RAM

Cost **\$26 MILLION**

CAN SOLVE
SAME PROBLEM IN

0.002 SECNODS

A row of black server racks in a data center. The racks are arranged in a perspective view, receding into the distance. The floor is a light-colored tile with a grid pattern. The text "HAVE A LOOK AT TOP 500" is overlaid in the center in a bold, yellow, sans-serif font. The word "TOP" is significantly larger than "500".

**HAVE A LOOK AT
TOP 500**

PROBLEM DECOMPOSITION

PROBLEM



GRANULARITY



Problem

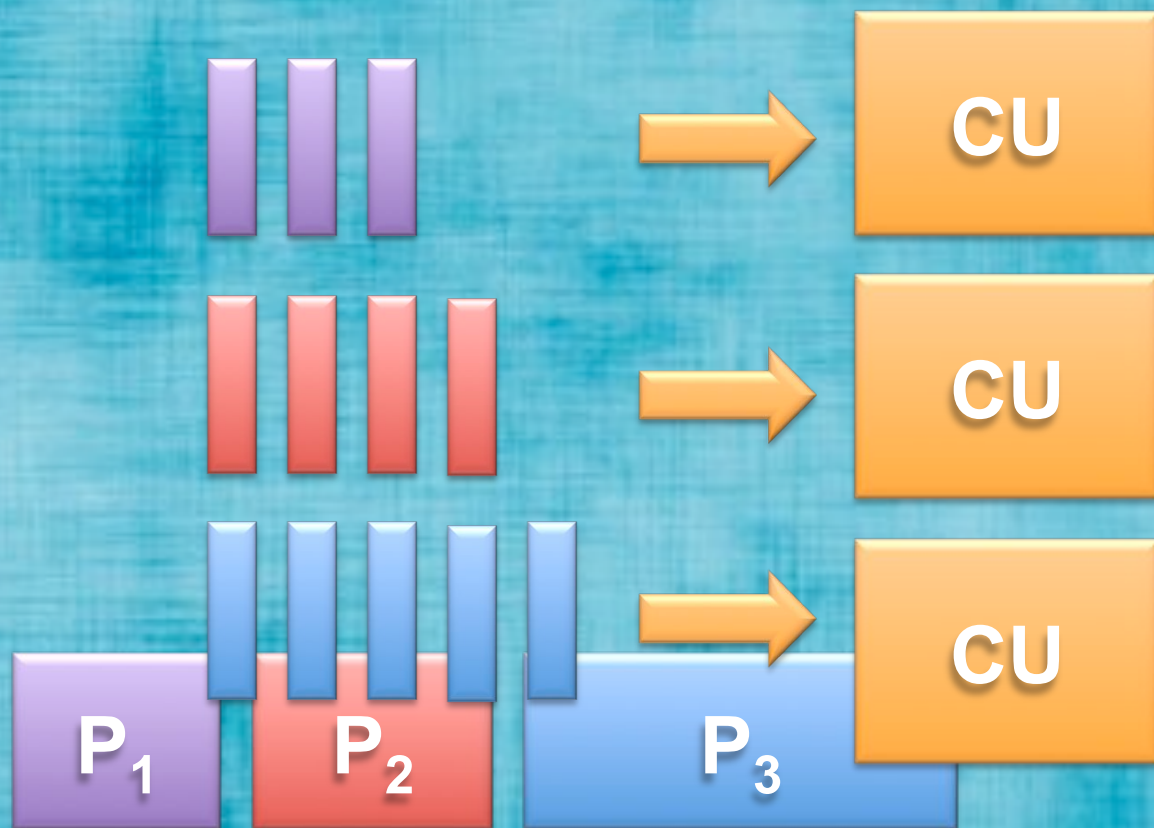


CU

P_1

P_2

P_3



TERMINOLOGY SO FAR

TASK

PARALLEL TASK

SERIAL EXECUTION

PARALLEL (CONCURRENT) EXECUTION

SINGLE VS. MULTICORE PROCESSORS

GRANULARITY

OBSERVED SPEEDUP